

2PC4617

NPN general-purpose transistor

Rev. 04 — 25 November 2004

Product data sheet

1. Product profile

1.1 General description

NPN transistor in a SOT416 (SC-75) plastic package. The PNP complement is 2PA1774.

1.2 Features

- Low current (max. 150 mA)
- Low voltage (max. 50 V).

1.3 Applications

General-purpose switching and amplification in communication, Electronic Data Processing (EDP) and consumer applications.

2. Pinning information

Table 1: Pinning

	9		
Pin	Description	Simplified outline	Symbol
1	base		_
2	emitter	——————————————————————————————————————	3
3	collector		1—
		1	2 sym021
			39111021

3. Ordering information

Table 2: Ordering information

Type number	Package				
	Name	Description	Version		
2PC4617Q	SC-75	plastic surface mounted package; 3 leads	SOT416		
2PC4617R	-				
2PC4617S					





4. Marking

Table 3: Marking codes

Type number	Marking code
2PC4617Q	ZQ
2PC4617R	ZR
2PC4617S	ZS

5. Limiting values

Table 4: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	60	V
V_{CEO}	collector-emitter voltage	open base	-	50	V
V_{EBO}	emitter-base voltage	open collector	-	7	V
I _C	collector current (DC)		-	150	mA
I _{CM}	peak collector current		-	200	mA
I_{BM}	peak base current		-	200	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$	[1] _	150	mW
T_{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C

^[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.

6. Thermal characteristics

Table 5: Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		<u>[1]</u> -	-	833	K/W

^[1] Transistor mounted on an FR4 printed-circuit board, single-sided copper, tin-plated and standard footprint.



7. Characteristics

Table 6: Characteristics

 T_{amb} = 25 °C; unless otherwise specified.

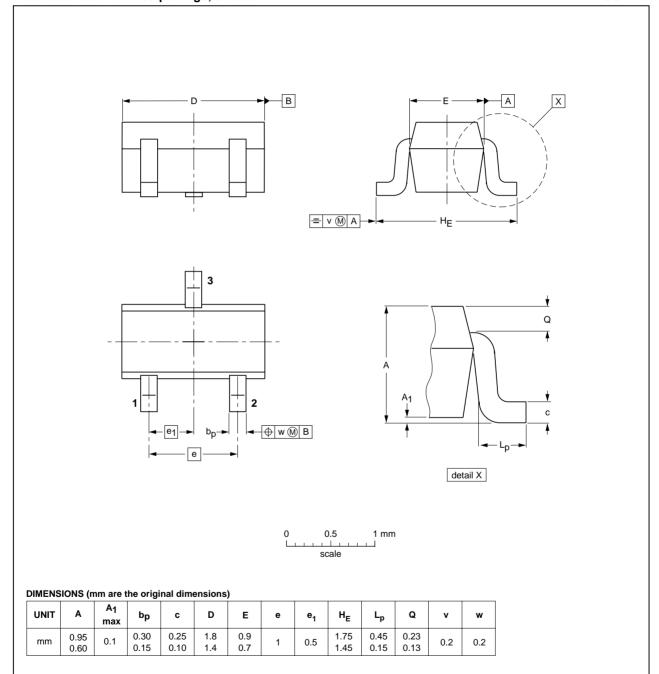
	<u> </u>	<u> </u>				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$I_E = 0 A; V_{CB} = 30 V$	-	-	100	nA
		$I_E = 0 \text{ A}; V_{CB} = 30 \text{ V};$ $T_j = 150 \text{ °C}$	-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	$I_C = 0 A; V_{EB} = 4 V$	-	-	100	nA
h _{FE}	DC current gain	$I_C = 1 \text{ mA}; V_{CE} = 6 \text{ V}$	<u>[1]</u>			
	2PC4617Q		120	-	270	
	2PC4617R		180	-	390	
	2PC4617S		270	-	560	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$	<u>[1]</u> _	-	200	mV
C _c	collector capacitance	$I_E = i_e = 0 \text{ A};$ $V_{CB} = 12 \text{ V}; f = 1 \text{ MHz}$	-	-	1.5	pF
f _T	transition frequency	$I_C = 2 \text{ mA}; V_{CE} = 12 \text{ V};$ f = 100 MHz	[<u>1</u>] 100	-	-	MHz

^[1] Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$



Plastic surface mounted package; 3 leads

SOT416



OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT416			SC-75		$\bigoplus \bigoplus$	97-02-28 04-11-04	

Fig 1. Package outline SOT416 (SC-75)



9. Revision history

Table 7: Revision history

	•				
Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
2PC4617_4	20041125	Product data sheet	-	9397 750 14085	2PC4617_3
Modifications:		t of this data sheet has b n standard of Philips Sen	•	comply with the nev	v presentation and
	 Section 1.3 	2: maximum low current	upgraded		
	• <u>Table 4</u> : V ₀	_{CBO} value changed to 60	V		
	 <u>Table 4</u>: V_I 	_{EBO} value changed to 7 V	1		
	• <u>Table 4</u> : I _C	value changed to 150 m	A.		
2PC4617_3	19990521	Product specification	-	9397 750 05959	2PC4617_2
2PC4617_2	19980721	Product specification	-	9397 750 04113	2PC4617_1
2PC4617_1	19970709	Product specification	-	9397 750 02011	-

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